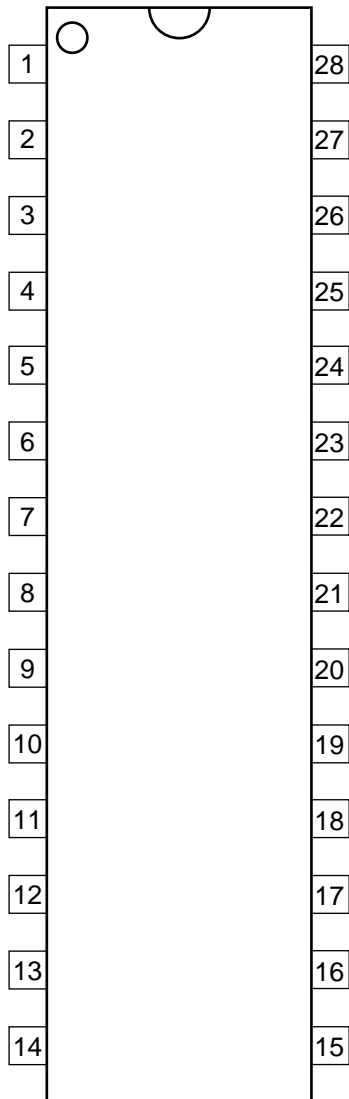


DUAL 24-BIT A/D CONVERTER  
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	VREF	15	O	SDATA
2	—	GNDL	16	I/O	FSYNC
3	O	VCOML	17	I	MCLK
4	I	AINL+	18	I	*CMODE/DFS
5	I	AINL-	19	I	HPFE
6	I	ZCAL	20	I	TEST
7	—	VD	21	—	BGND
8	—	DGND	22	—	AGND
9	O	CAL	23	—	VA
10	O	$\overline{\text{RST}}$	24	I	AINR-
11	O	SMODE2	25	I	AINR+
12	O	SMODE1	26	O	VCOMR
13	I/O	LRCK	27	—	GNDR
14	I/O	SCLK	28	O	VREFR

\*PIN-18  
CMODE : AK5392  
DFS : AK5393

**INPUTS**

AINL+	: L-CH ANALOG, POSITIVE
AINL-	: L-CH ANALOG, NEGATIVE
AINR+	: R-CH ANALOG, POSITIVE
AINR-	: R-CH ANALOG, NEGATIVE
CMODE	: MASTER CLOCK SELECT (L : 256fs/H : 384fs)
DFS	: DOUBLE FAST SAMPLING MODE SELECT
HPFE	: HPF ENABLE
MCLK	: MASTER CLOCK
RST	: RESET
SMODE1, SMODE2	: SERIAL INTERFACE MODE SELECT
TEST	: TEST
ZCAL	: ZERO CALIBRATION

**OUTPUTS**

CAL	: CALIBRATION STATUS
SDATA	: SERIAL DATA
VCOML	: L-CH COMMON VOLTAGE
VCOMR	: R-CH COMMON VOLTAGE
VREFL	: L-CH REFERENCE VOLTAGE
VREFR	: R-CH REFERENCE VOLTAGE

**INPUTS/OUTPUTS**

FSYNC	: FRAME SYNC CLOCK
LRCK	: L/R CHANNEL SELECT CLOCK
SCLK	: SERIAL DATA CLOCK

**OTHERS**

A.GND	: ANALOG GROUND
B.GND	: BOARD GROUND
D.GND	: DIGITAL GROUND
GNDL, GNDR	: REFERENCE GROUND
VA	: ANALOG POWER SUPPLY
VD	: DIGITAL POWER SUPPLY

